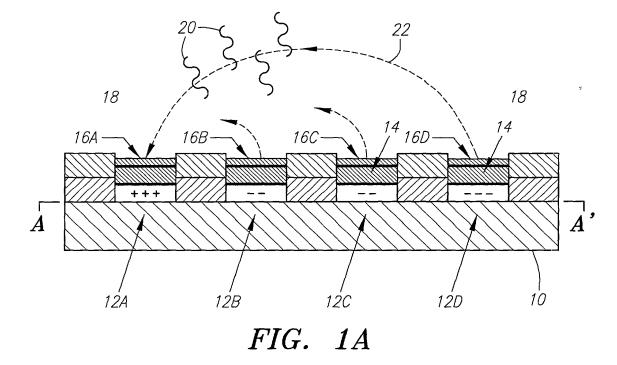
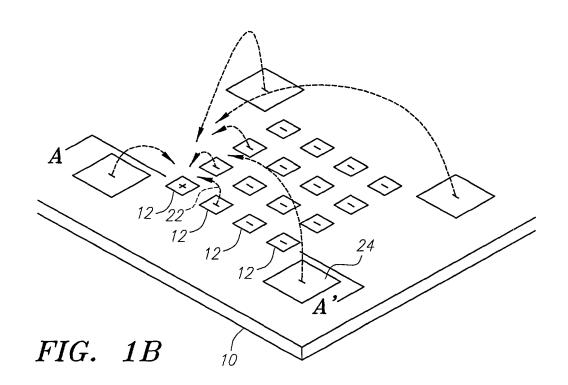
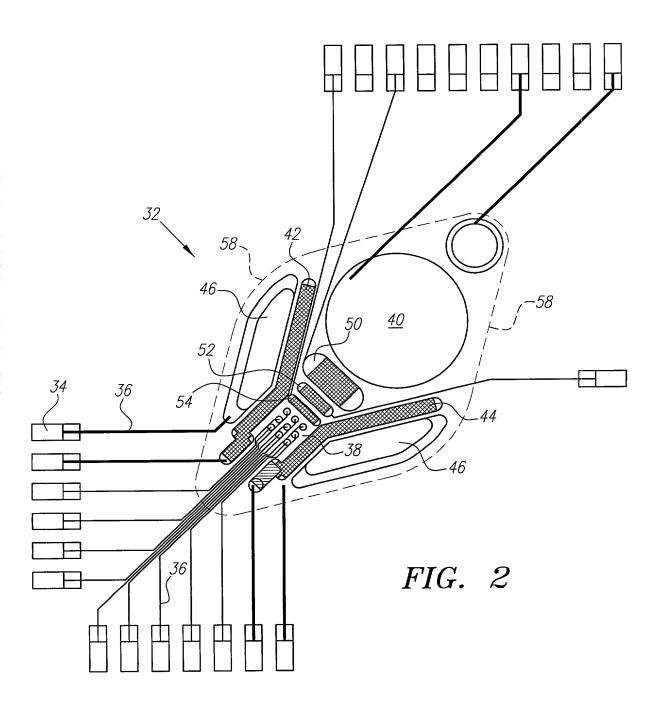
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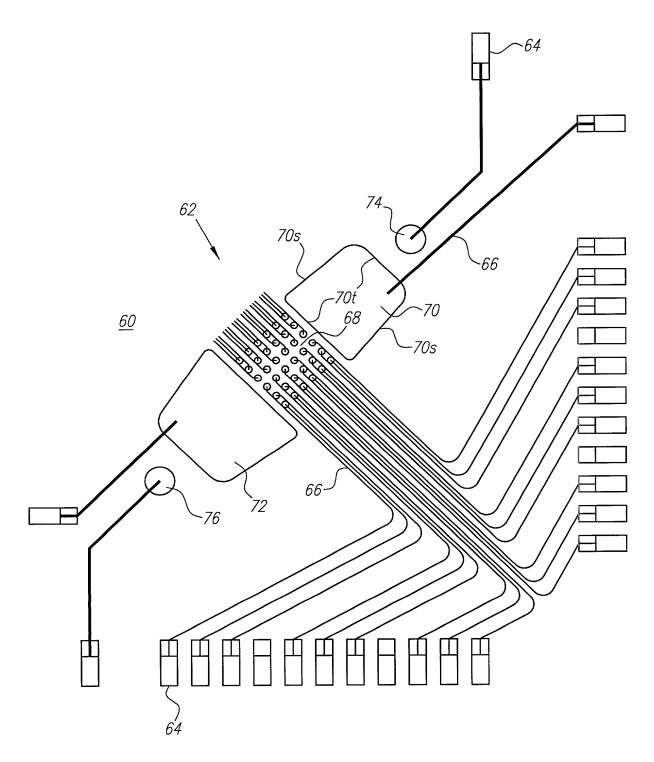


FIG. 3

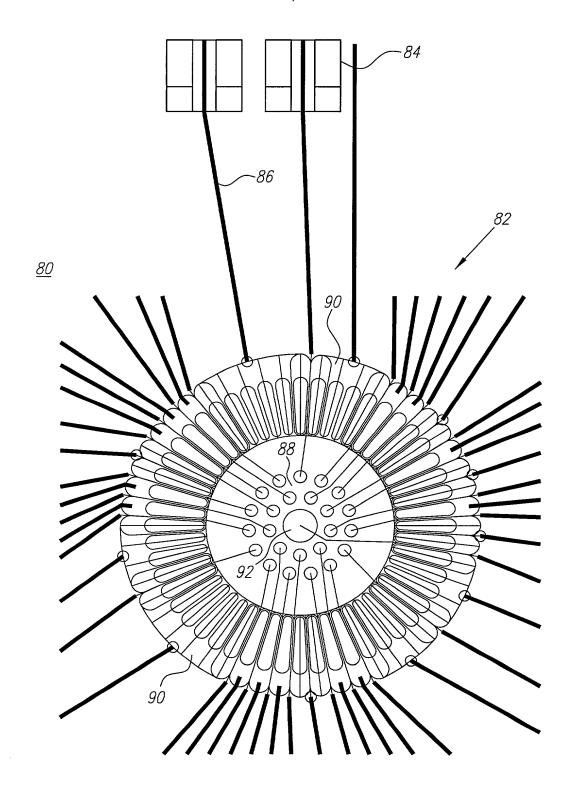
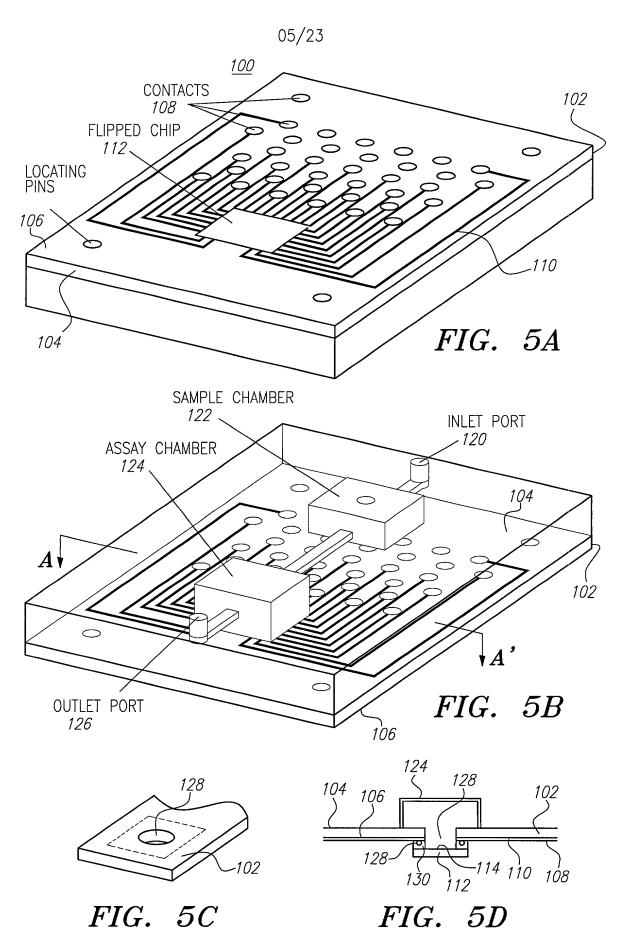


FIG. 4



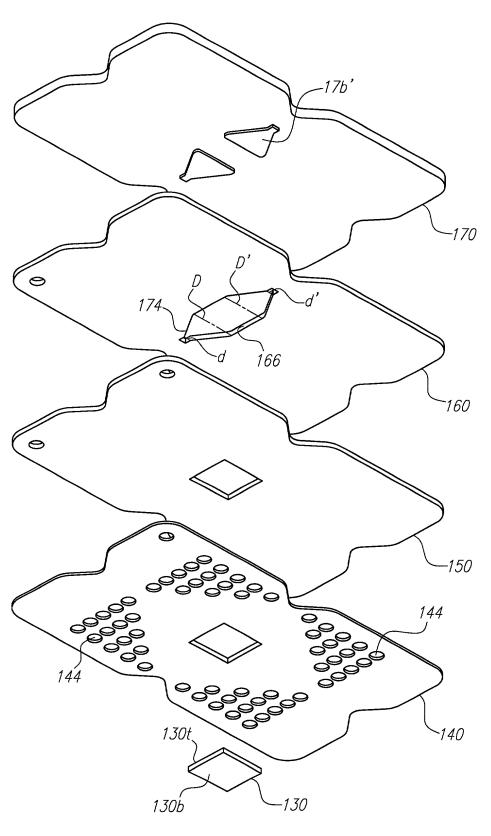
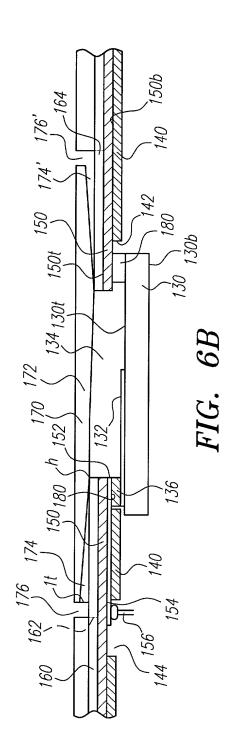


FIG. 6A



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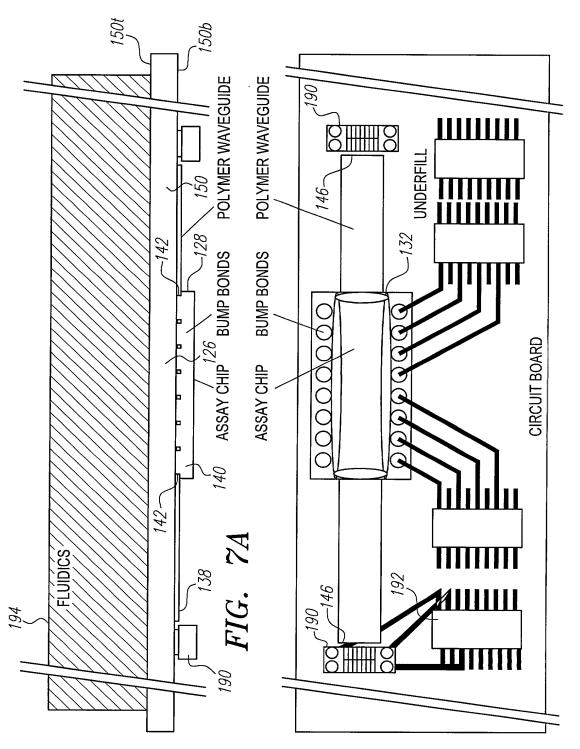


FIG. 7B

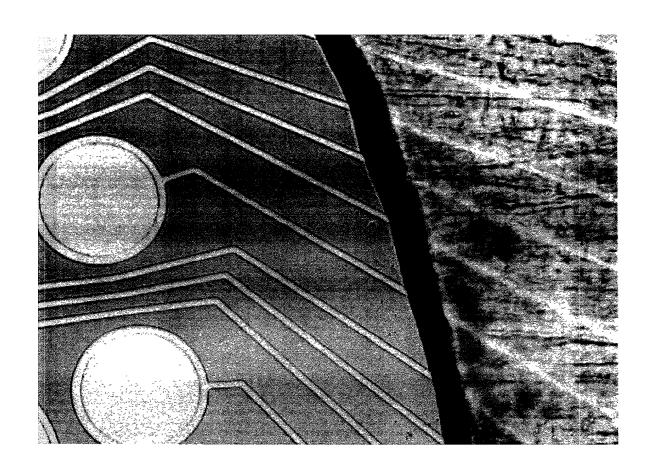
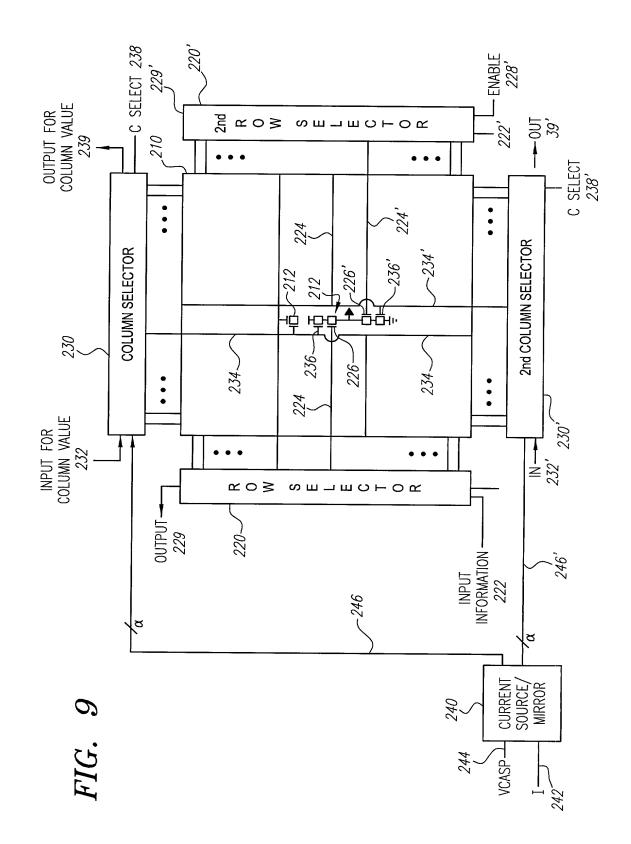


FIG. 8



11/23 284 - Vcc 262 -234 <u>212</u> 260 FIG. 10A 266 264 272 276 250 270 282 224 280 274 TUO I Ġ 274 270' 224' `276' 272' 264 234' 260' 266° 284' 262 290 OUTPUT 292 ENABLE/ CHIP SELECT 294 ROW 296b 296c 296d 296a () **SELECT** COLUMN **SELECT VOLTAGE**

FIG. 10B

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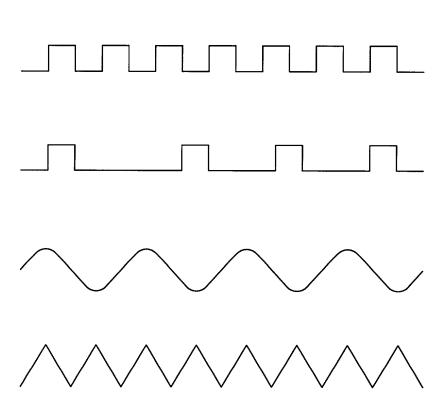
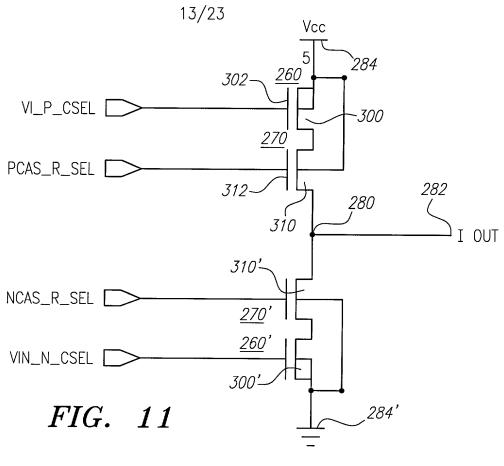
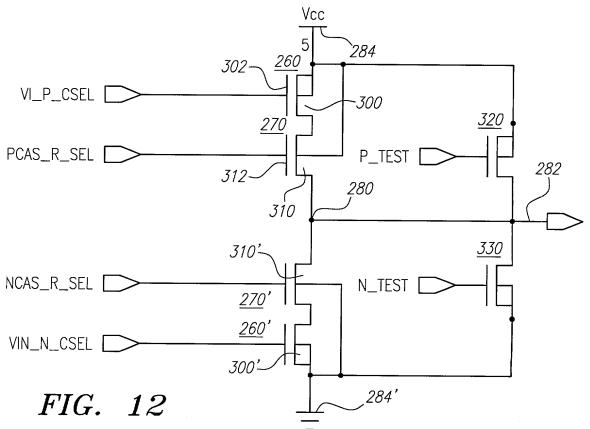
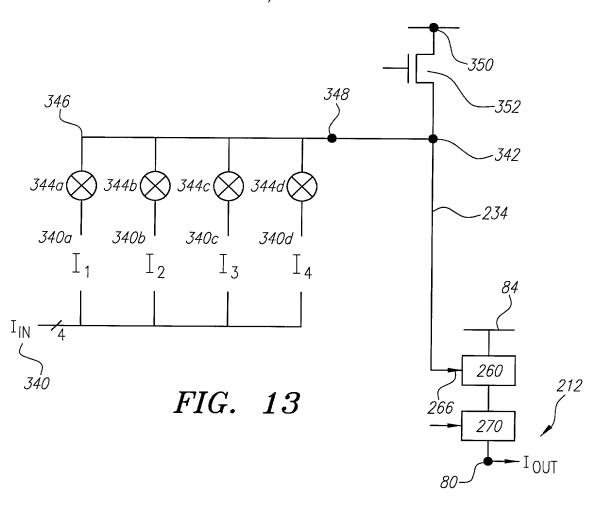


FIG. 10C







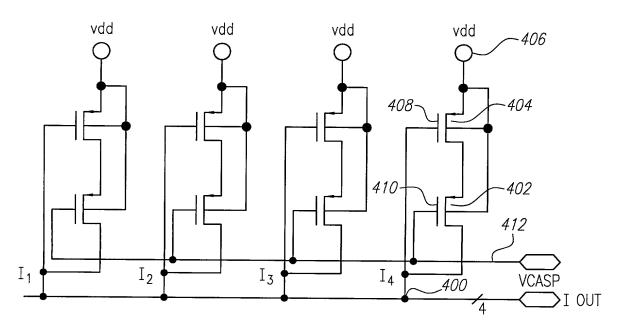
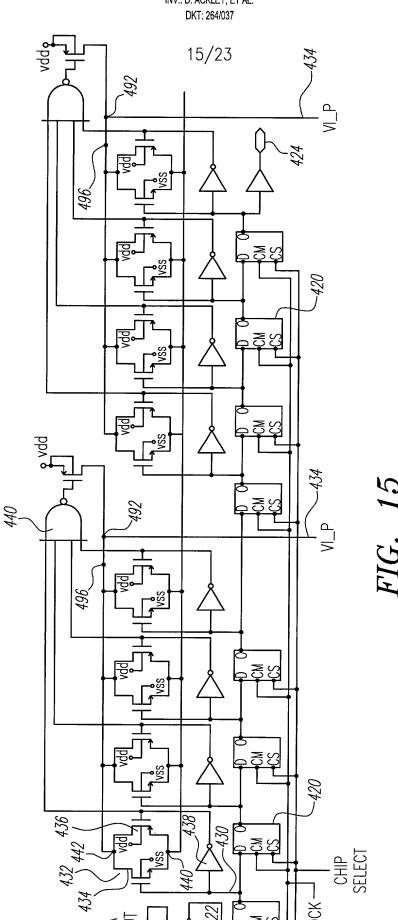


FIG. 14



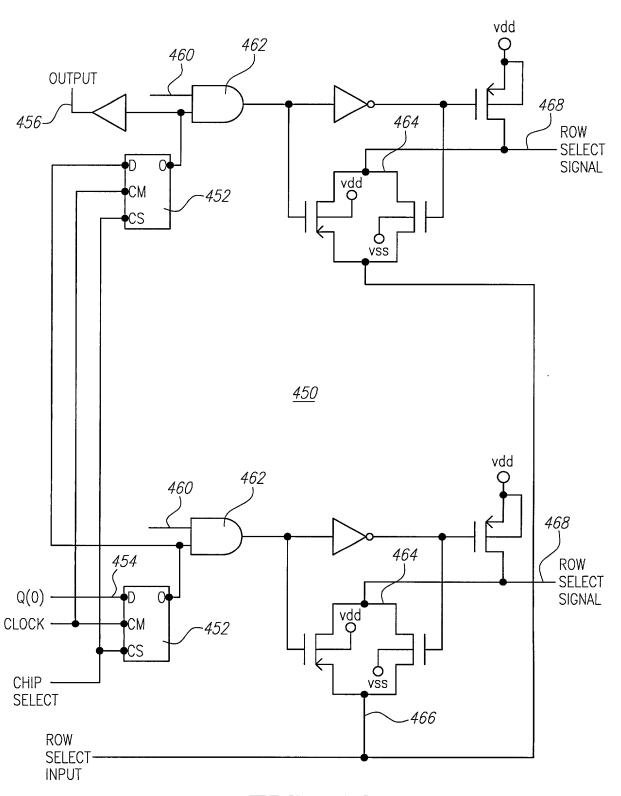


FIG. 16

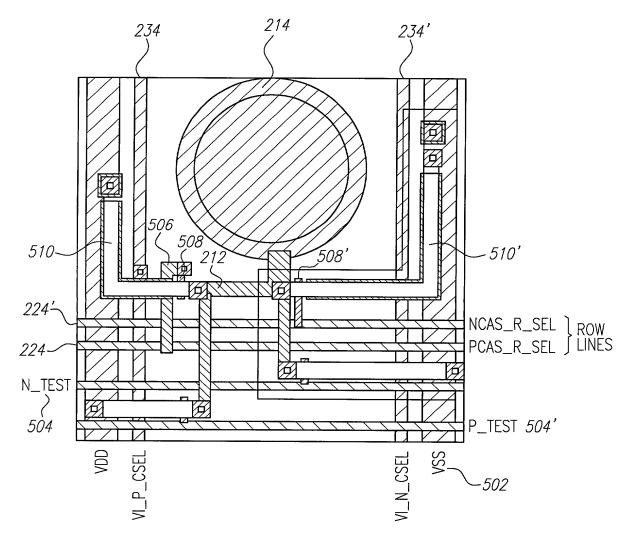


FIG. 17

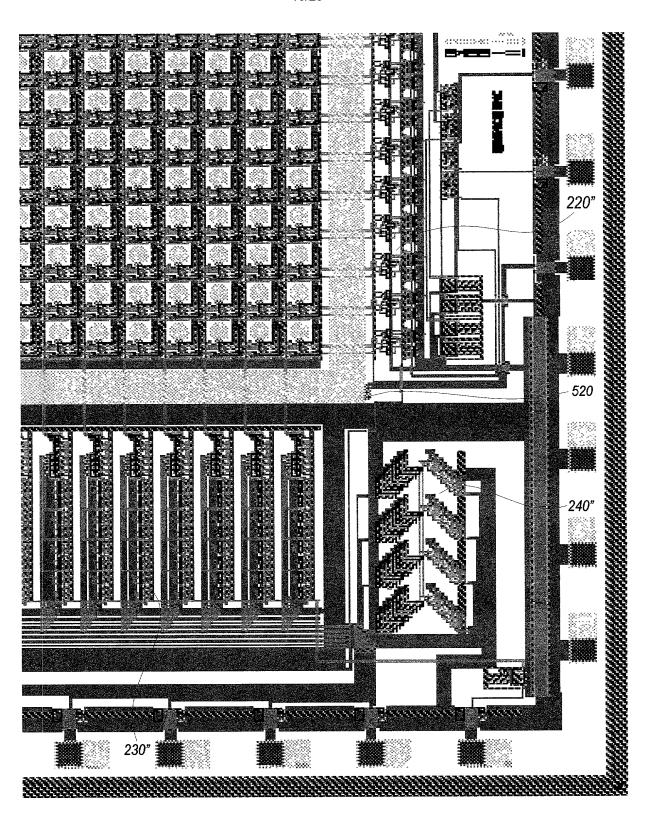
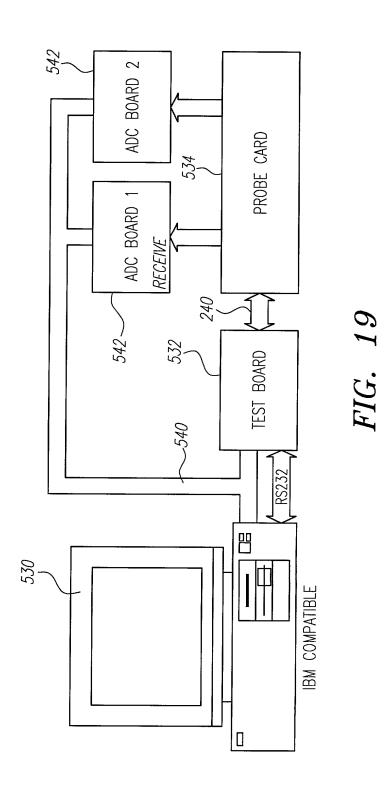
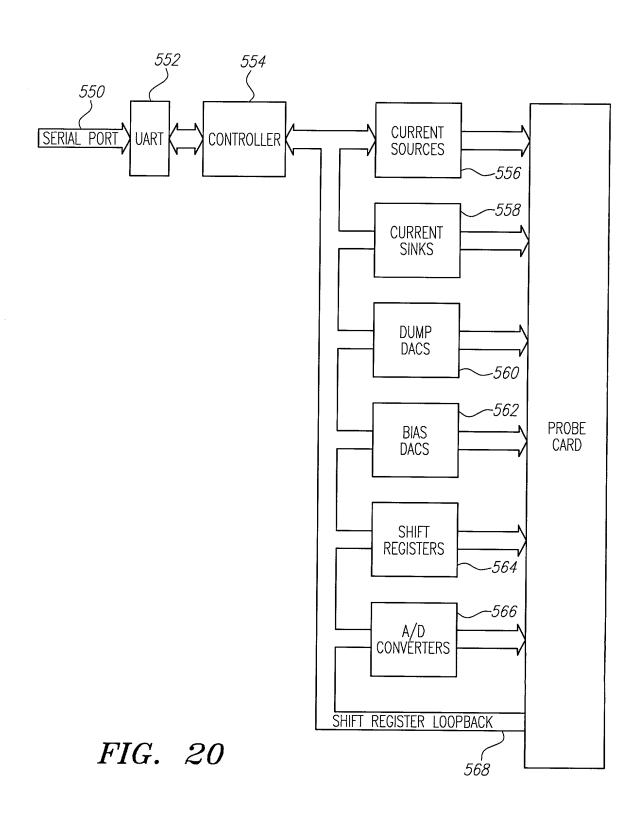


FIG. 18

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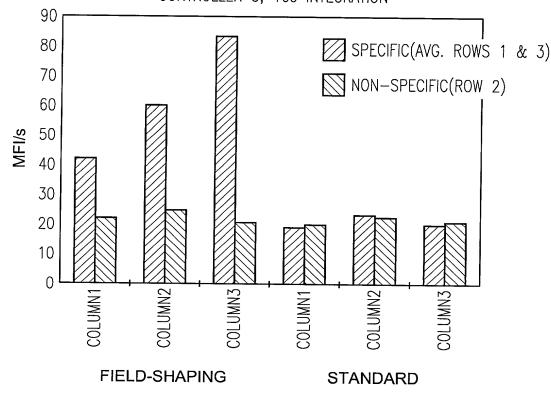




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ELECTRONIC HYBRIDIZATION: Q CHIP (ATA5/ATA7-BIOTIN, 10pM RCA5-BTR)

CONTROLLER 3, 10s INTEGRATION



TORKETUR INSINIT

FIG. 21

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